

FIG.1

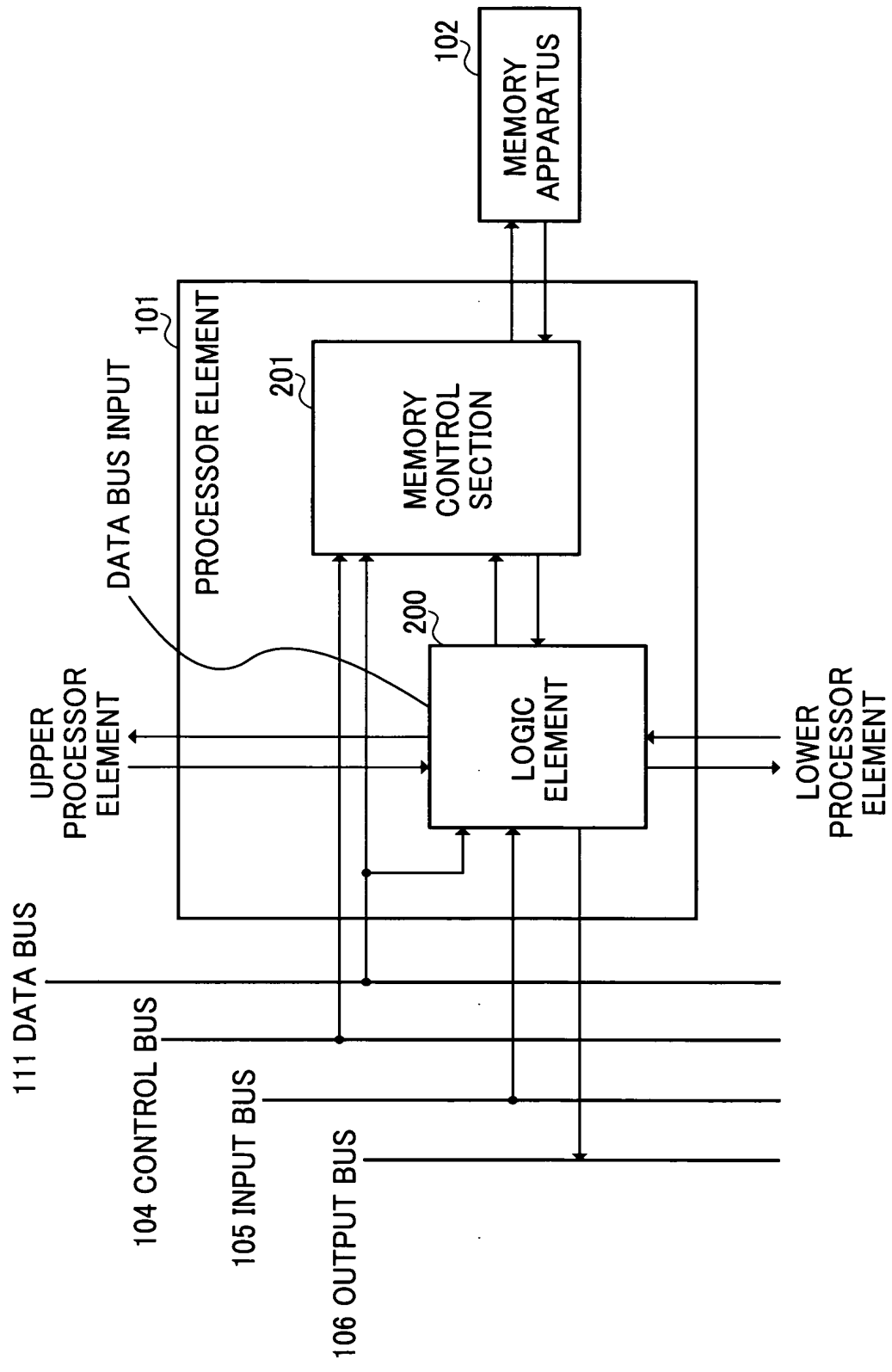


FIG.2

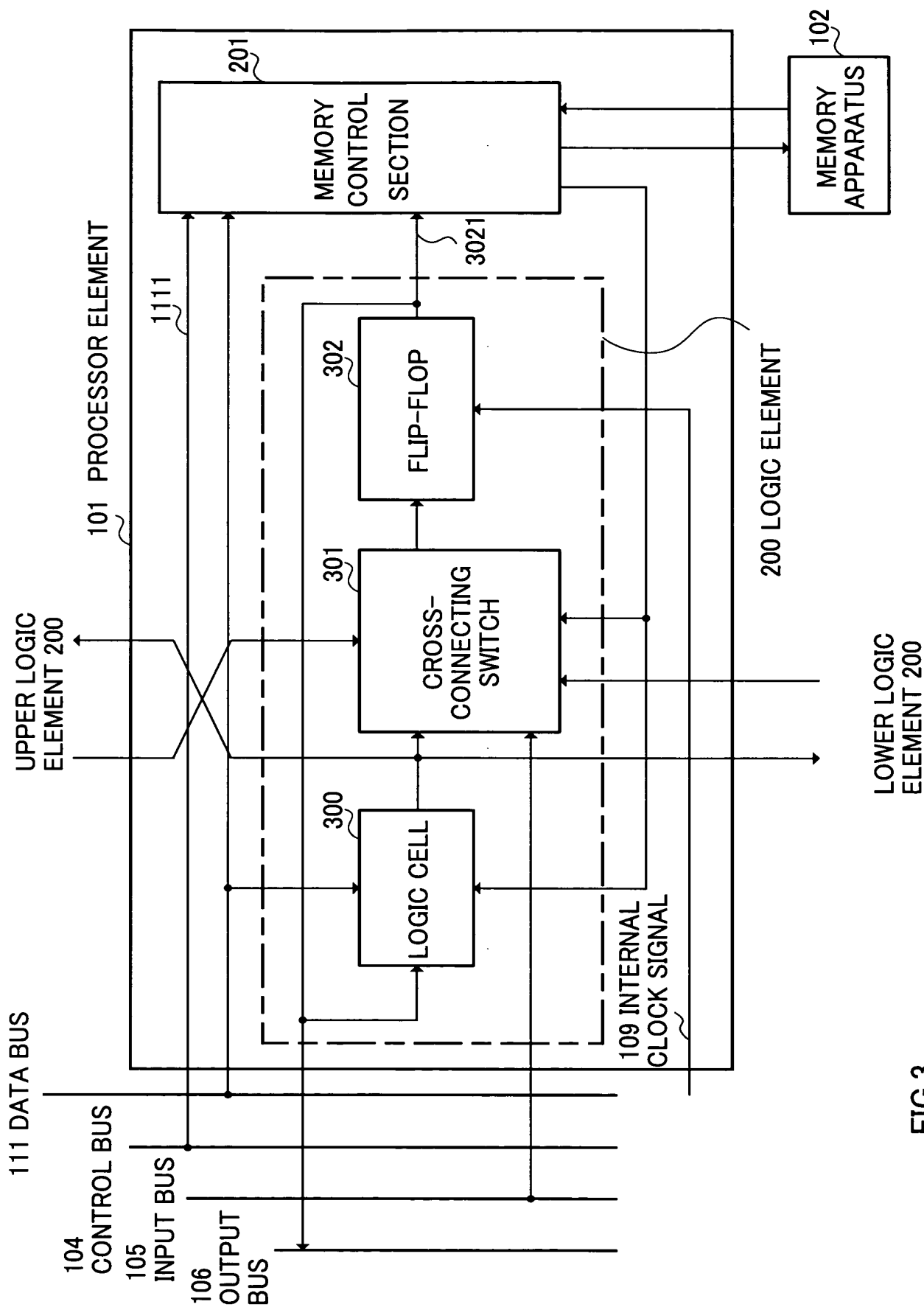


FIG.3

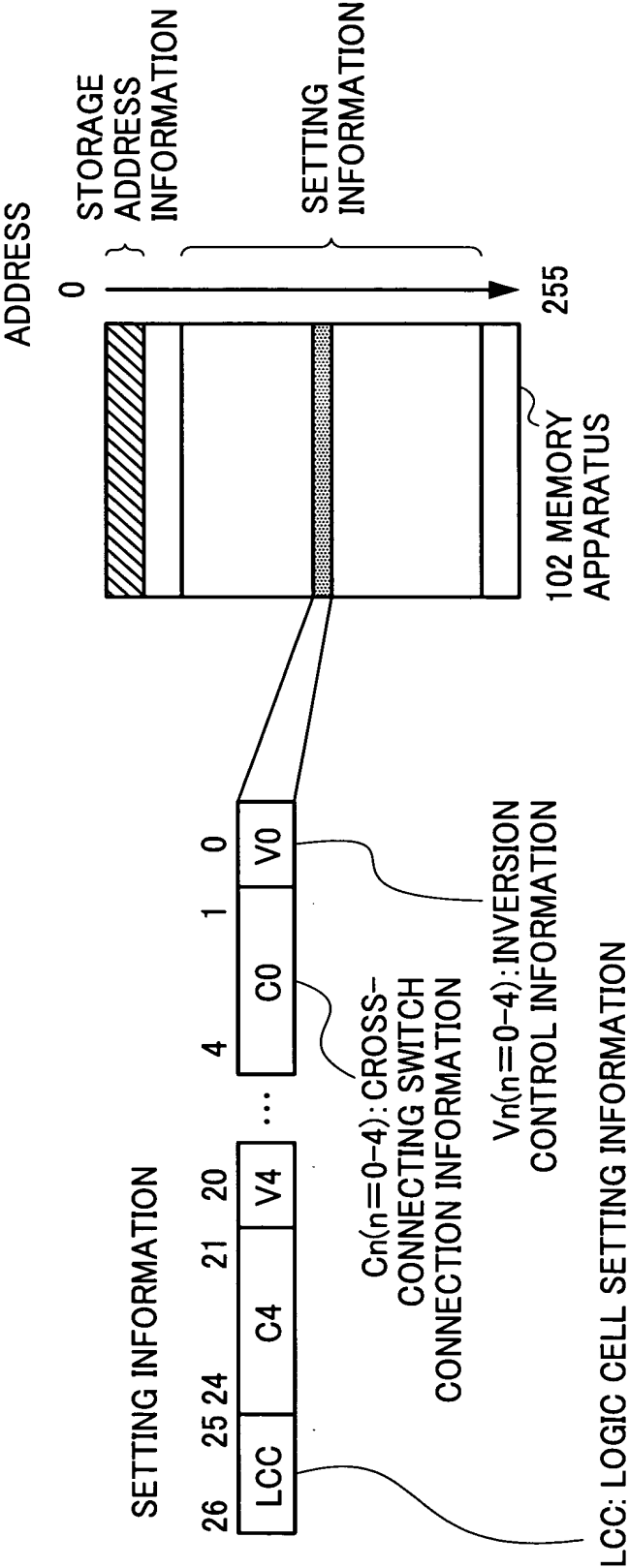


FIG.4

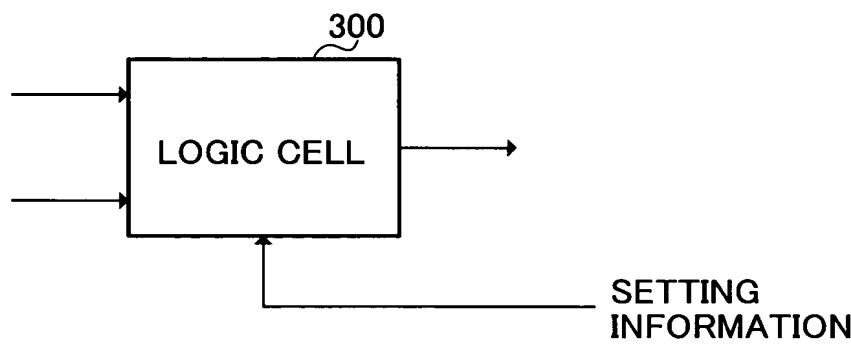


FIG.5

		SETTING INFORMATION			
		00	01	10	11
INPUT DATA	00	0	0	0	1
	01	1	0	1	0
	10	1	0	1	0
	11	1	1	0	0

FIG.6

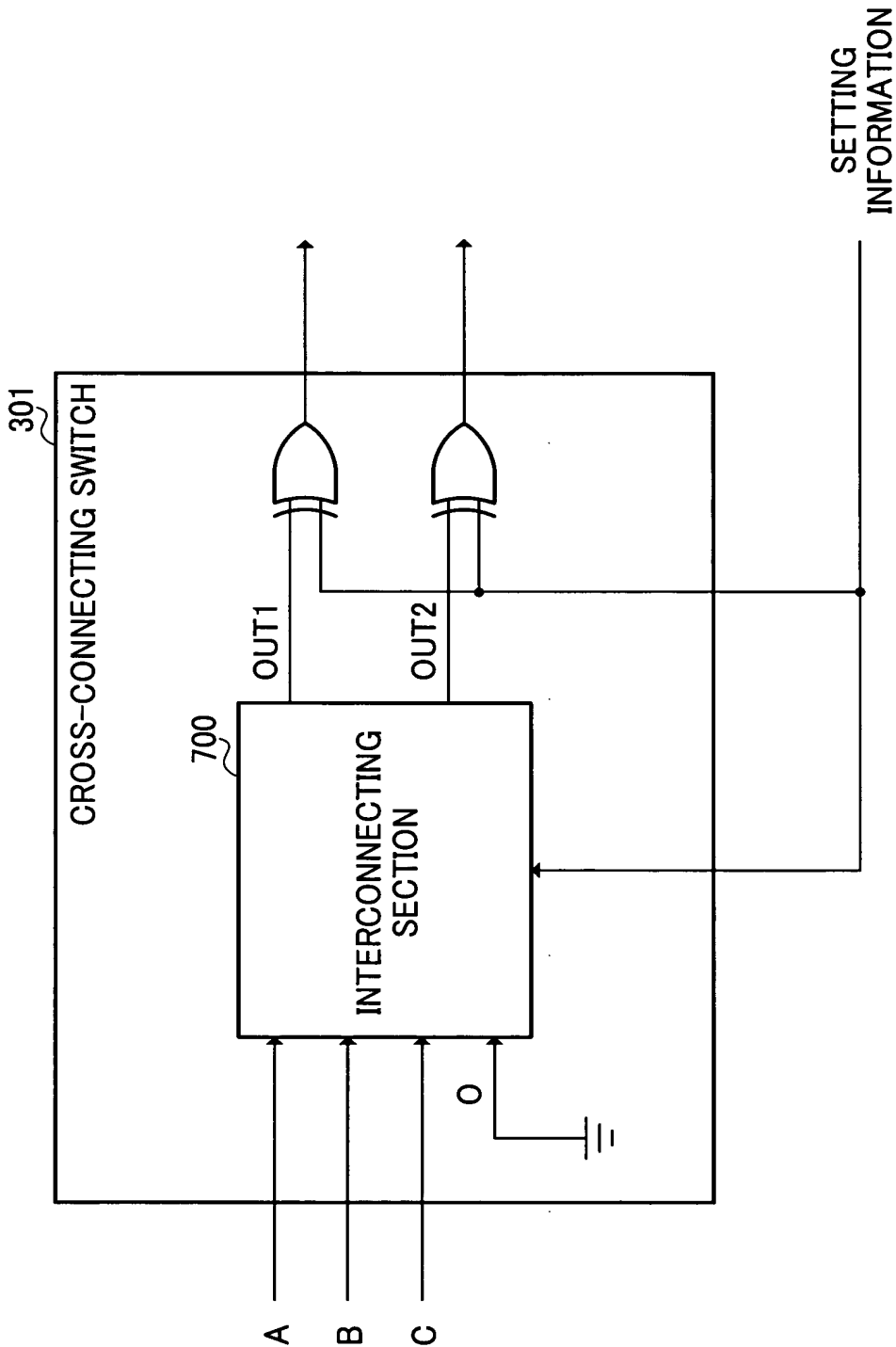


FIG.7

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		OUTPUT DATA	
		OUT1	OUT2
SETTING INFORMATION	0000	A	A
	0001	A	B
	0010	A	C
	0011	A	O
	0100	B	A
	0101	B	B
	0110	B	C
	0111	B	O
	1000	C	A
	1001	C	B
	1010	C	C
	1011	C	O
	1100	O	A
	1101	O	B
	1110	O	C
	1111	O	O

FIG.8

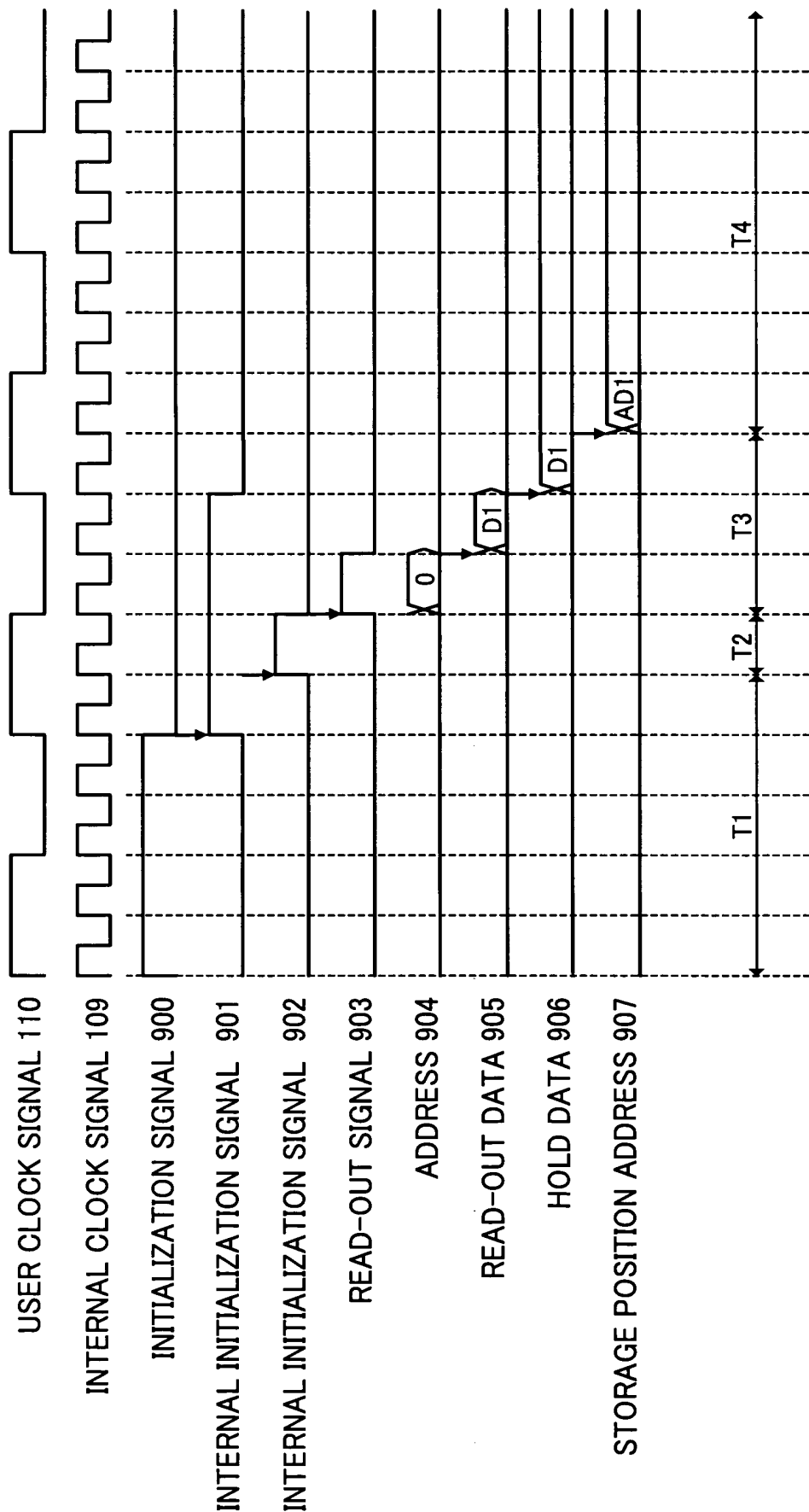


FIG.9

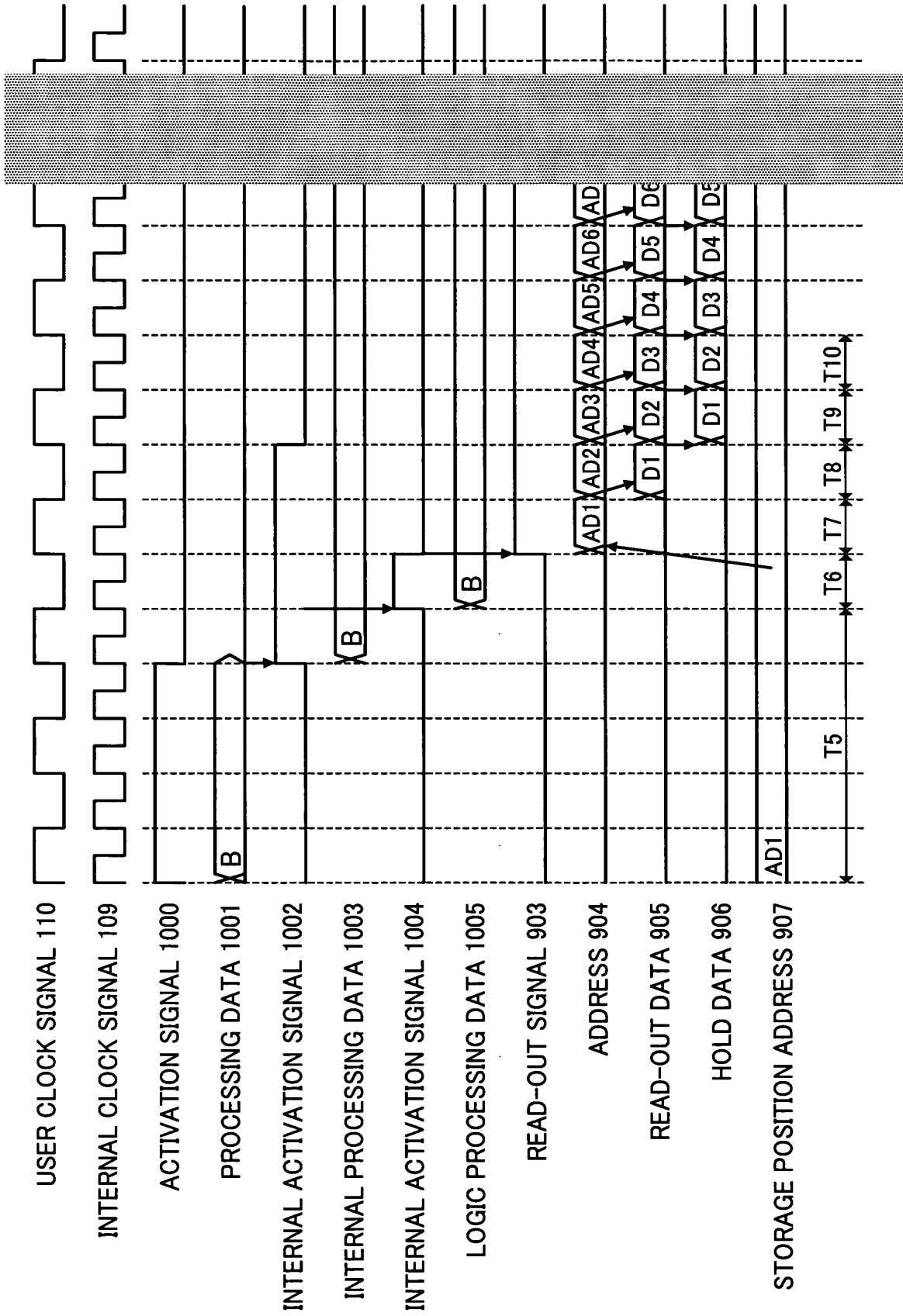
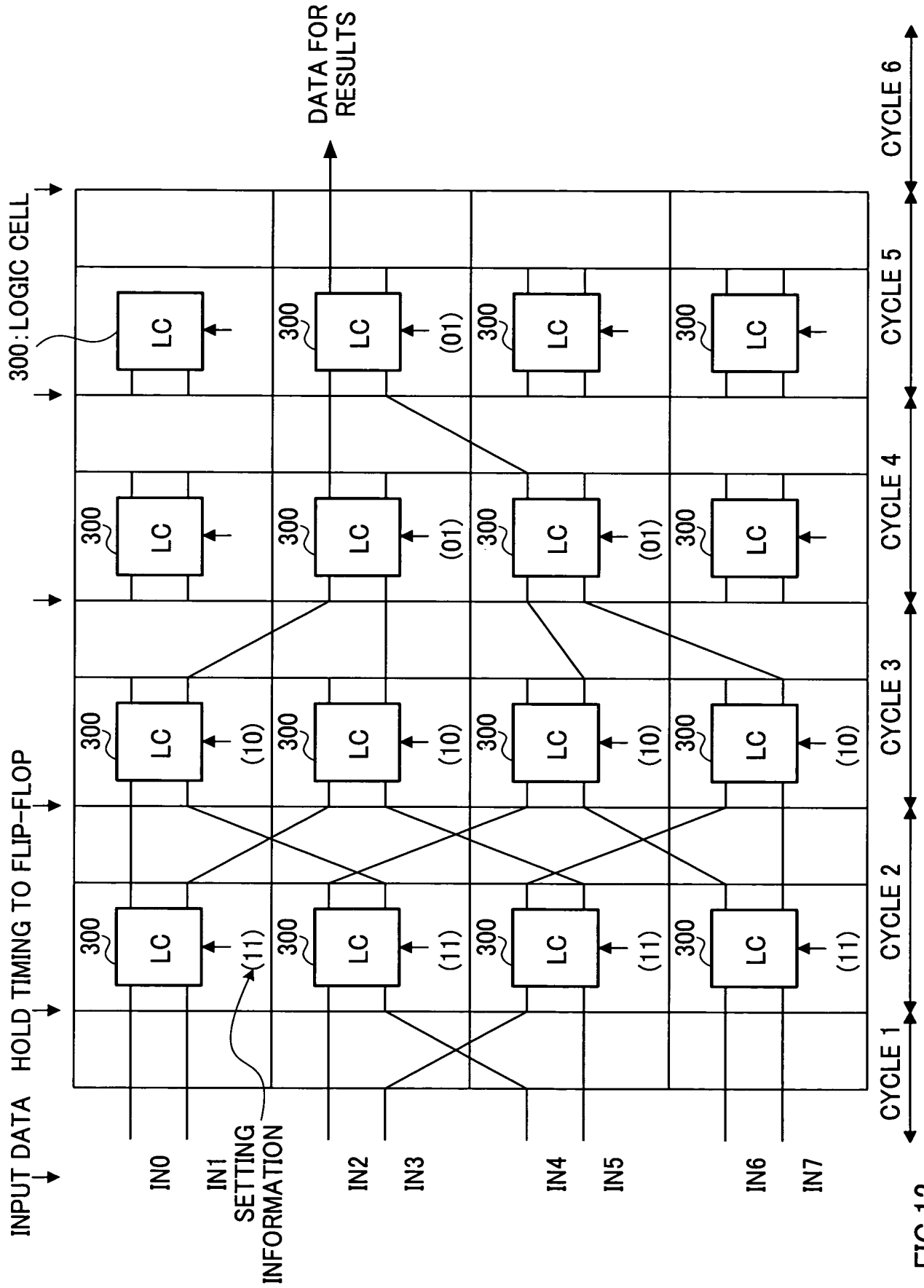


FIG.10

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		SETTING INFORMATION			
		00	01	10	11
INPUT DATA	00	01	01	01	00
	01	10	01	10	01
	10	10	01	10	10
	11	10	10	01	11

FIG.11



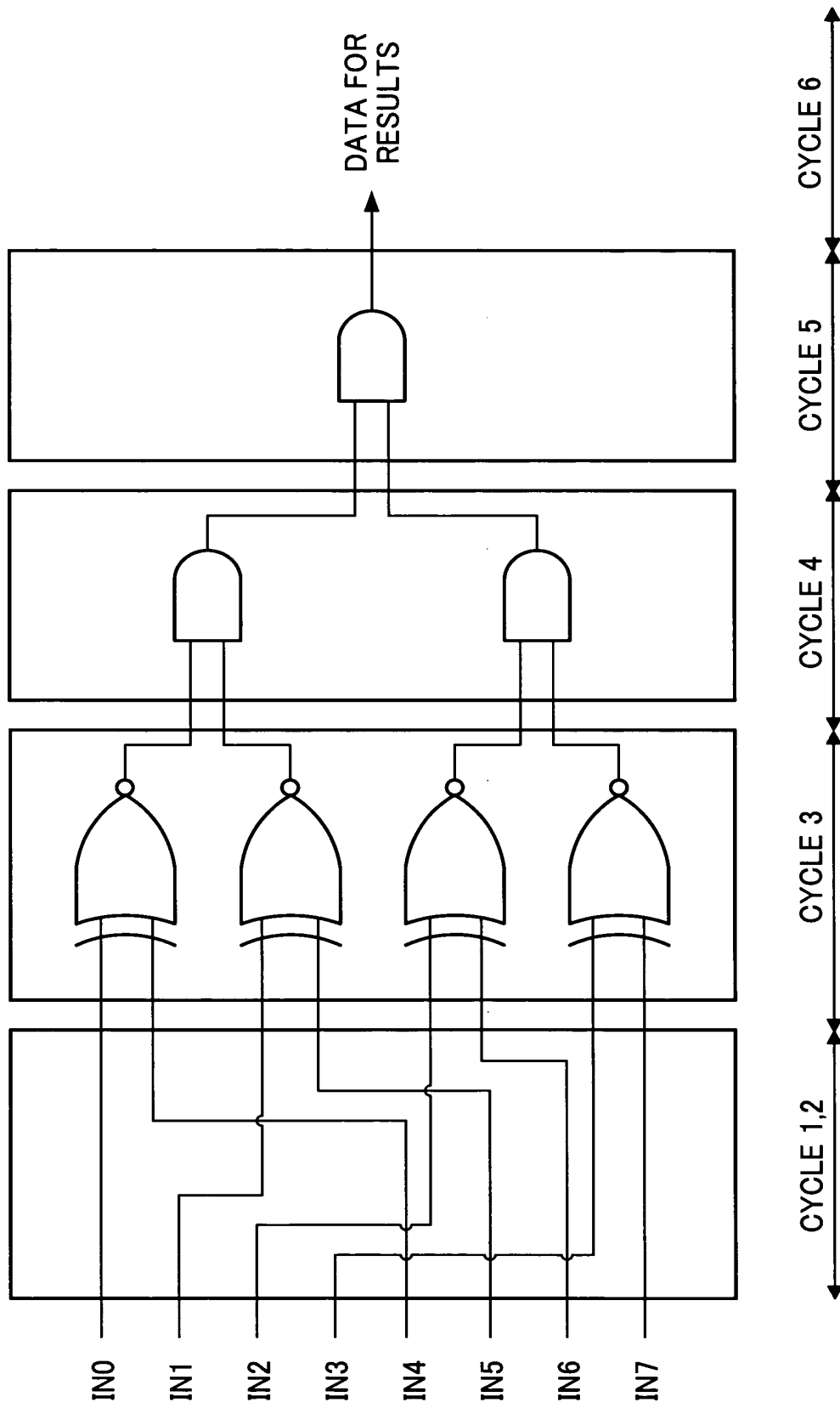


FIG.13

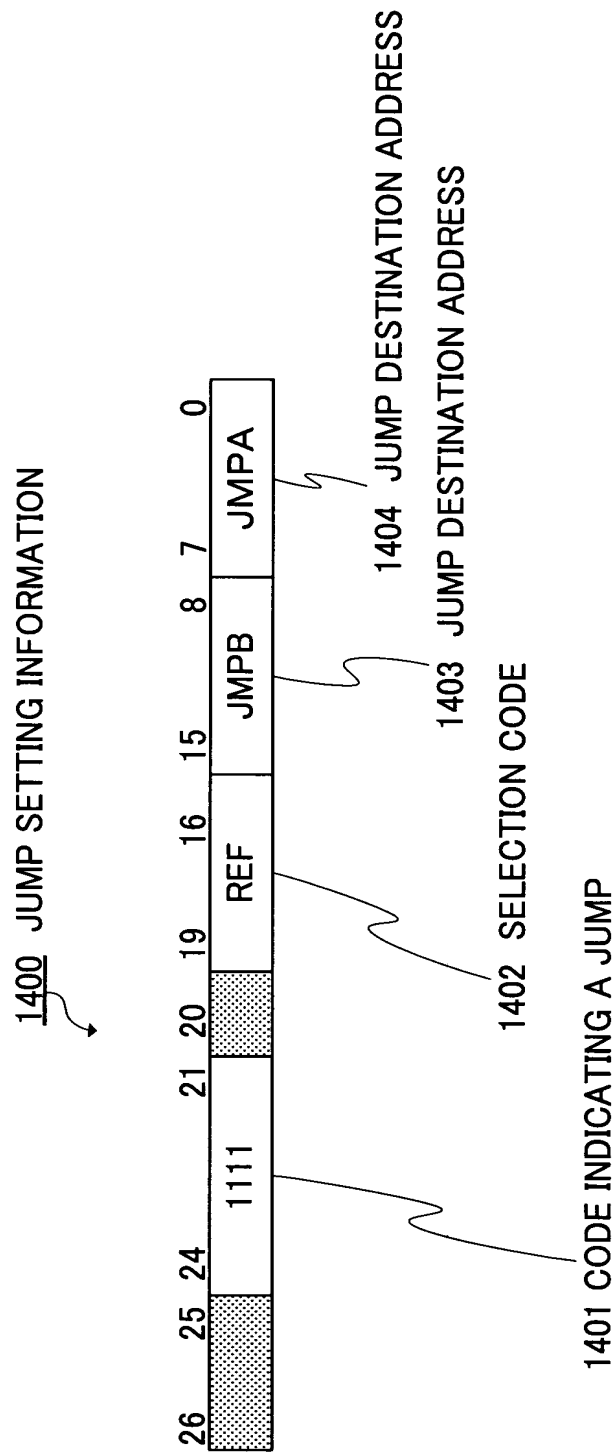


FIG.14

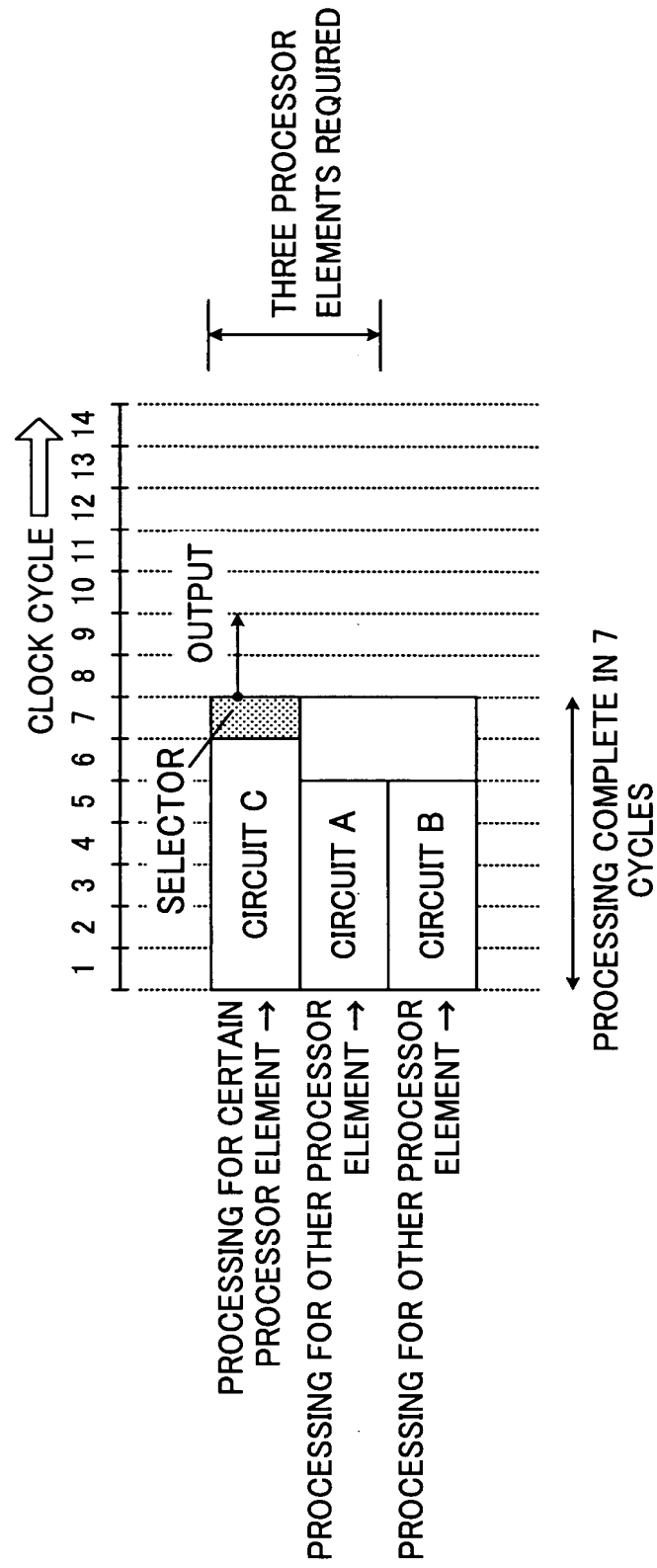


FIG.16

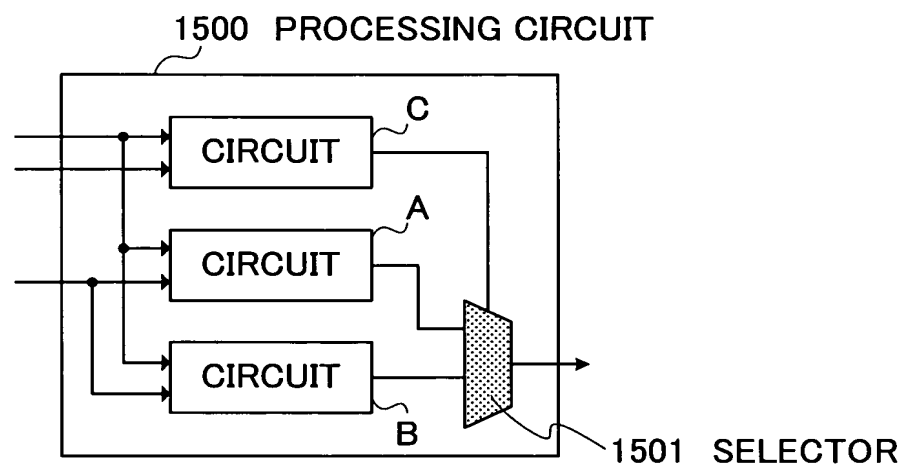


FIG.15

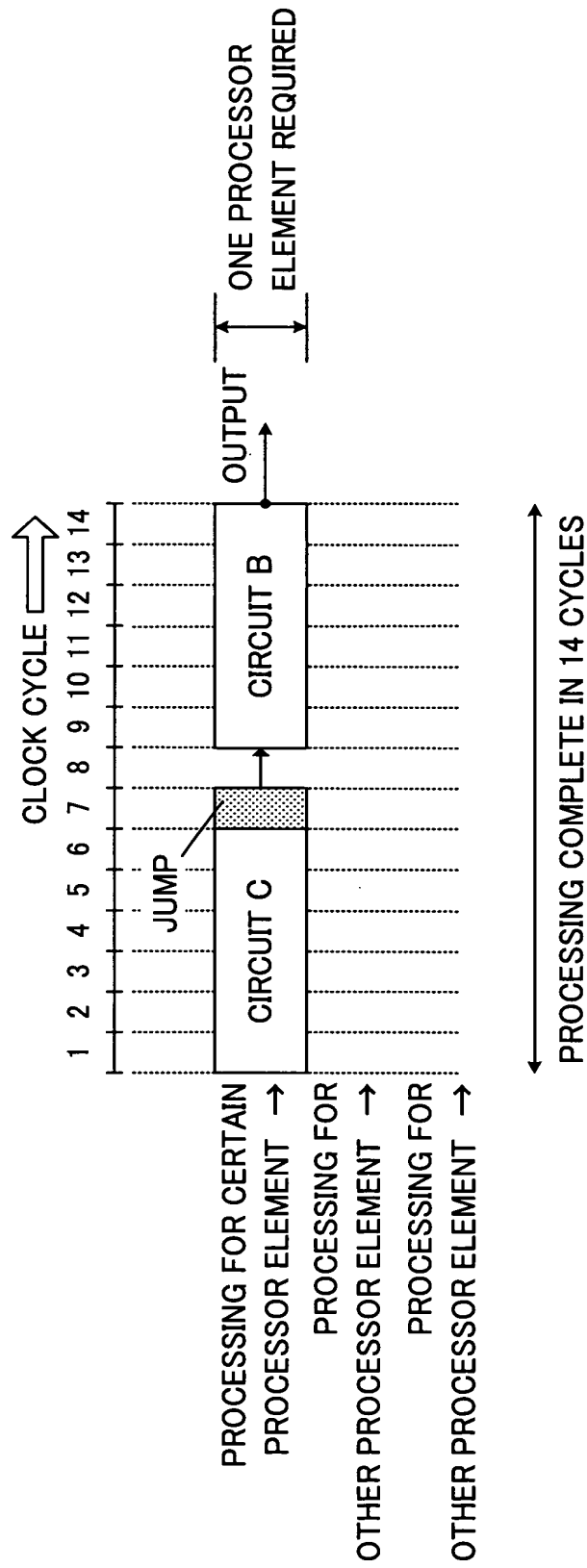


FIG.17

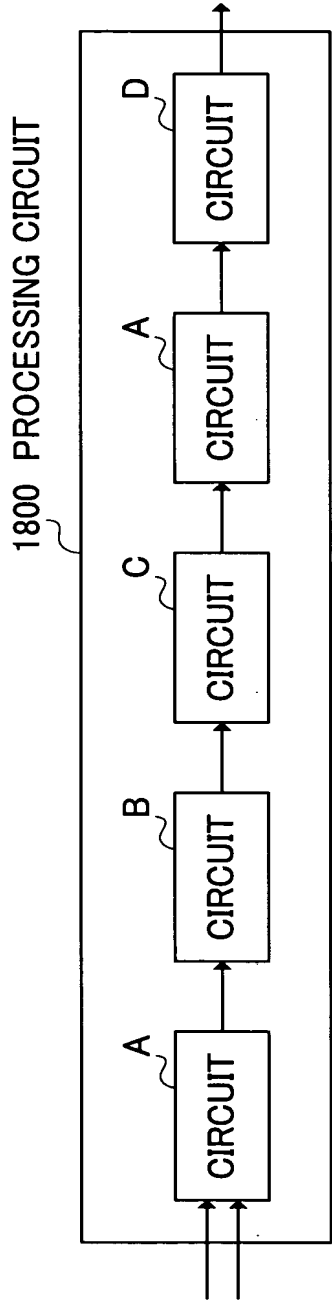


FIG.18

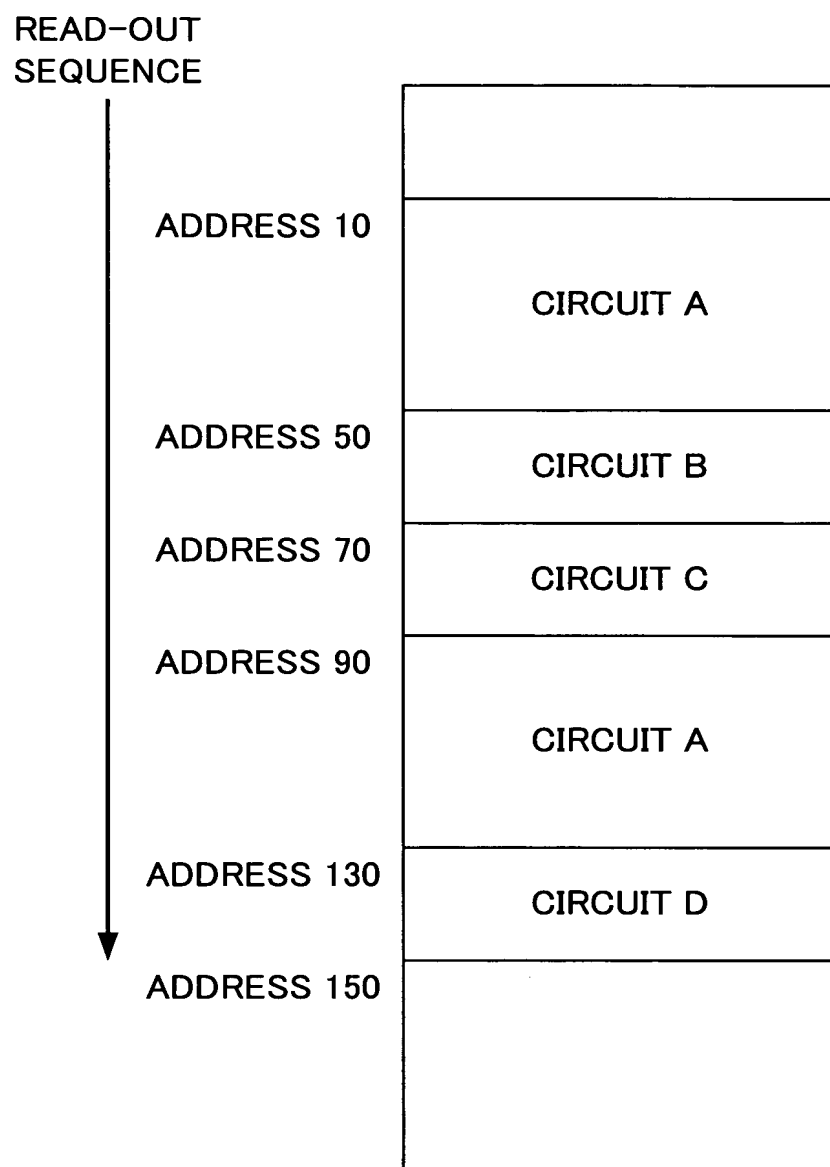


FIG.19

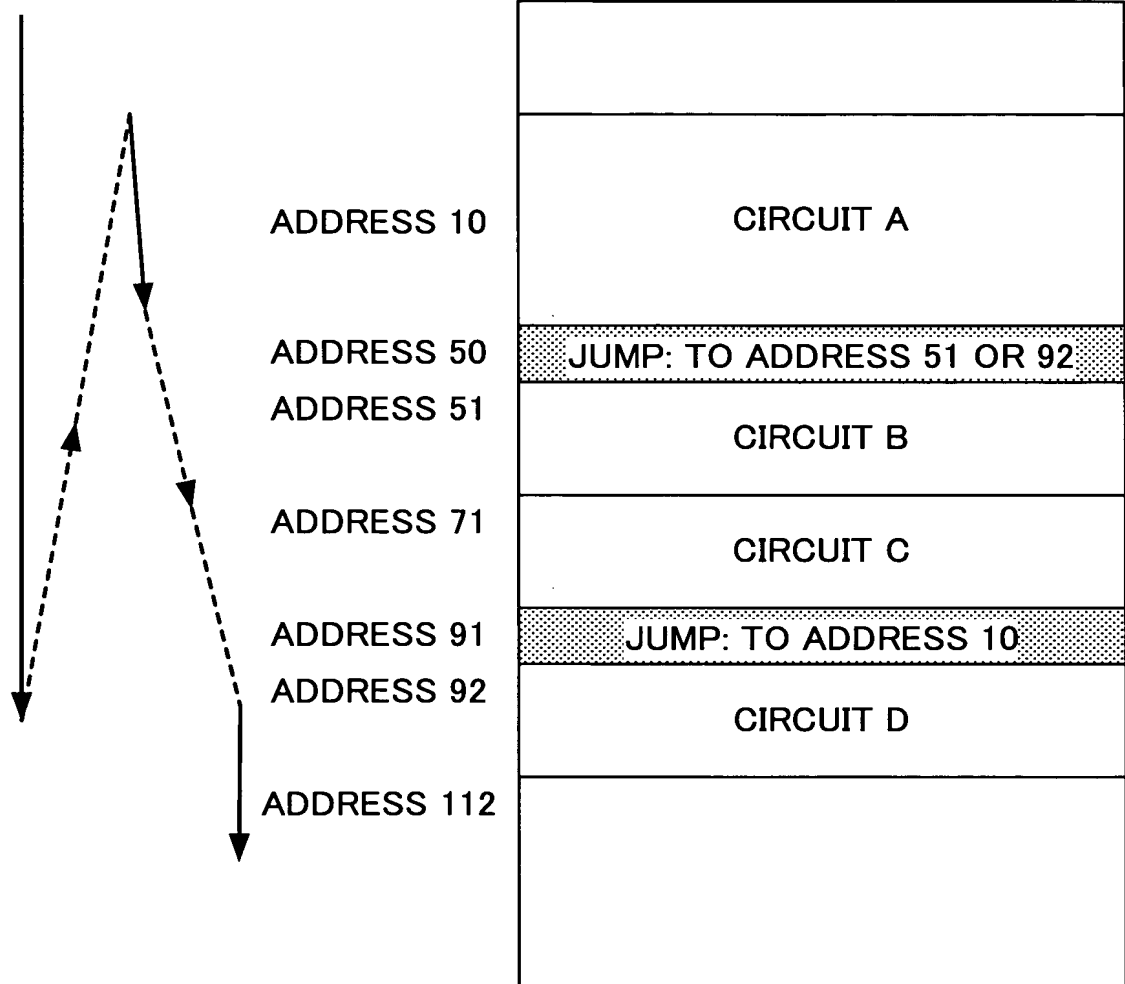
READ-OUT
SEQUENCE

FIG.20

2100 STOPPING SETTING INFORMATION

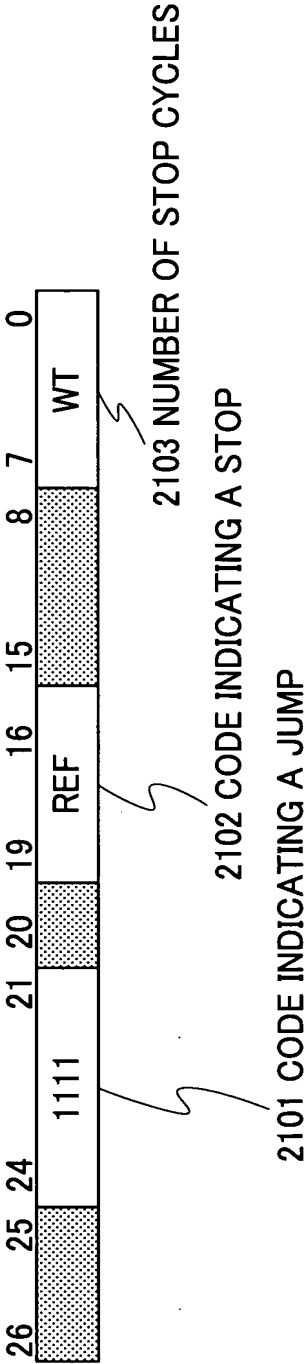


FIG.21

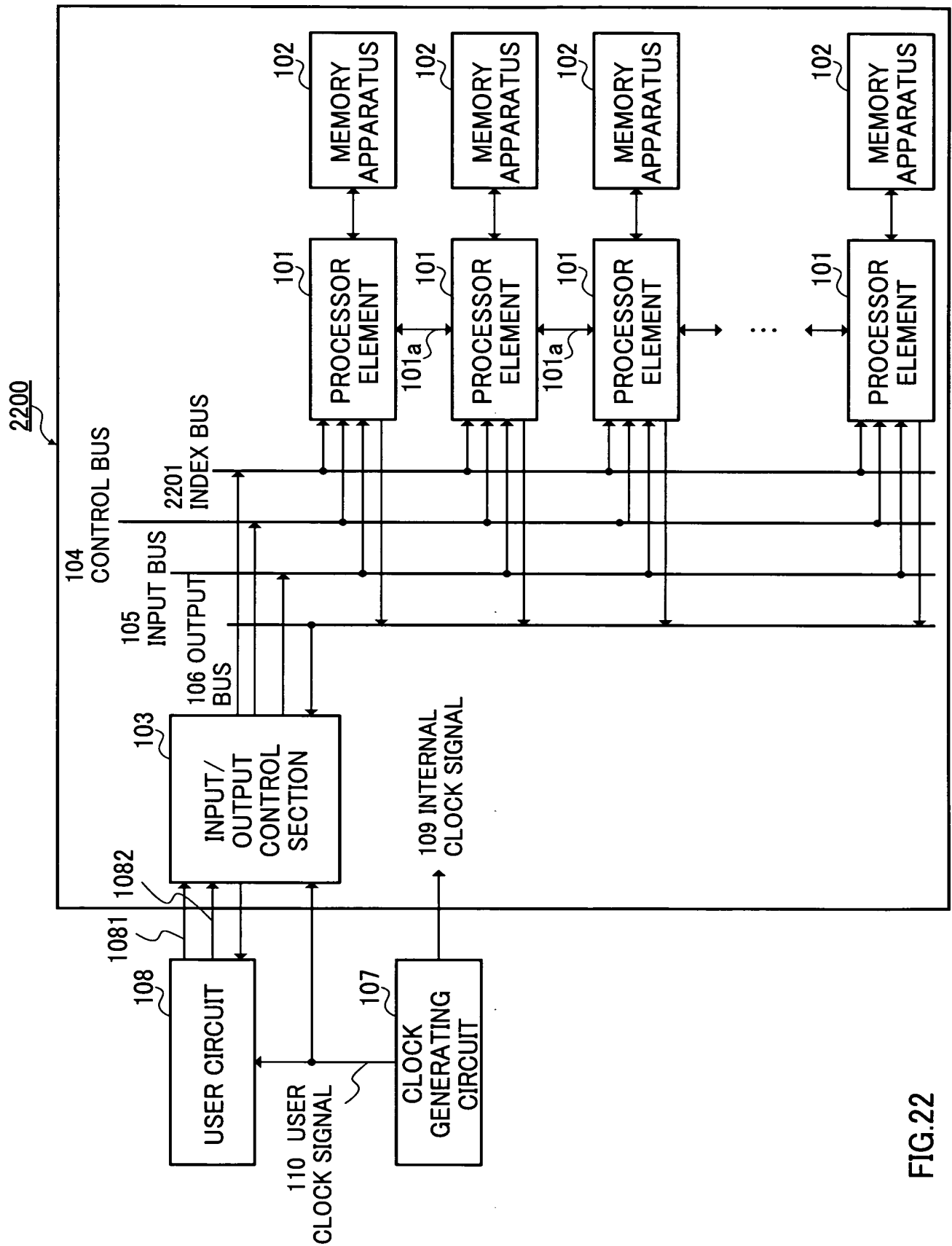


FIG.22

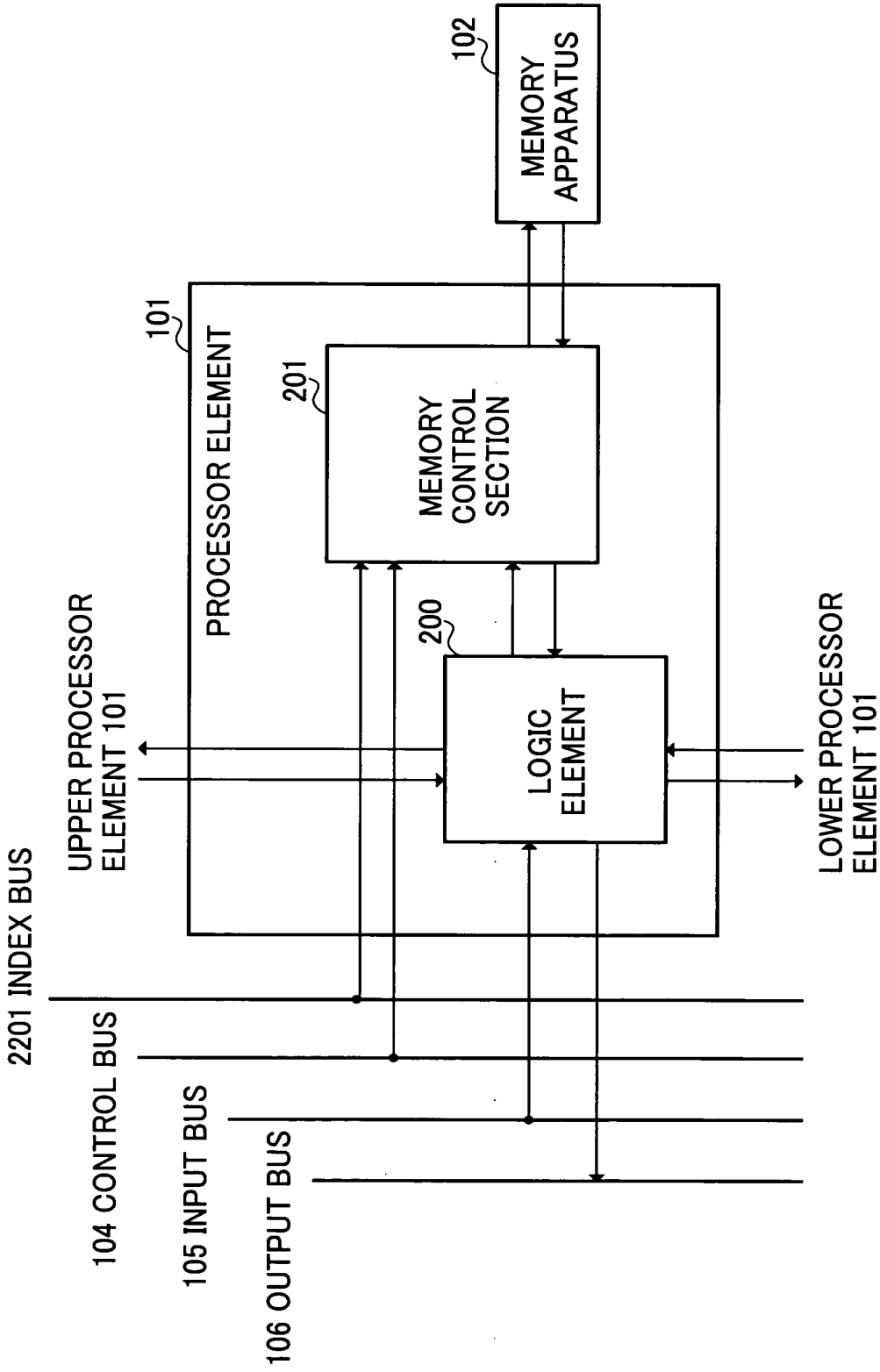


FIG.23

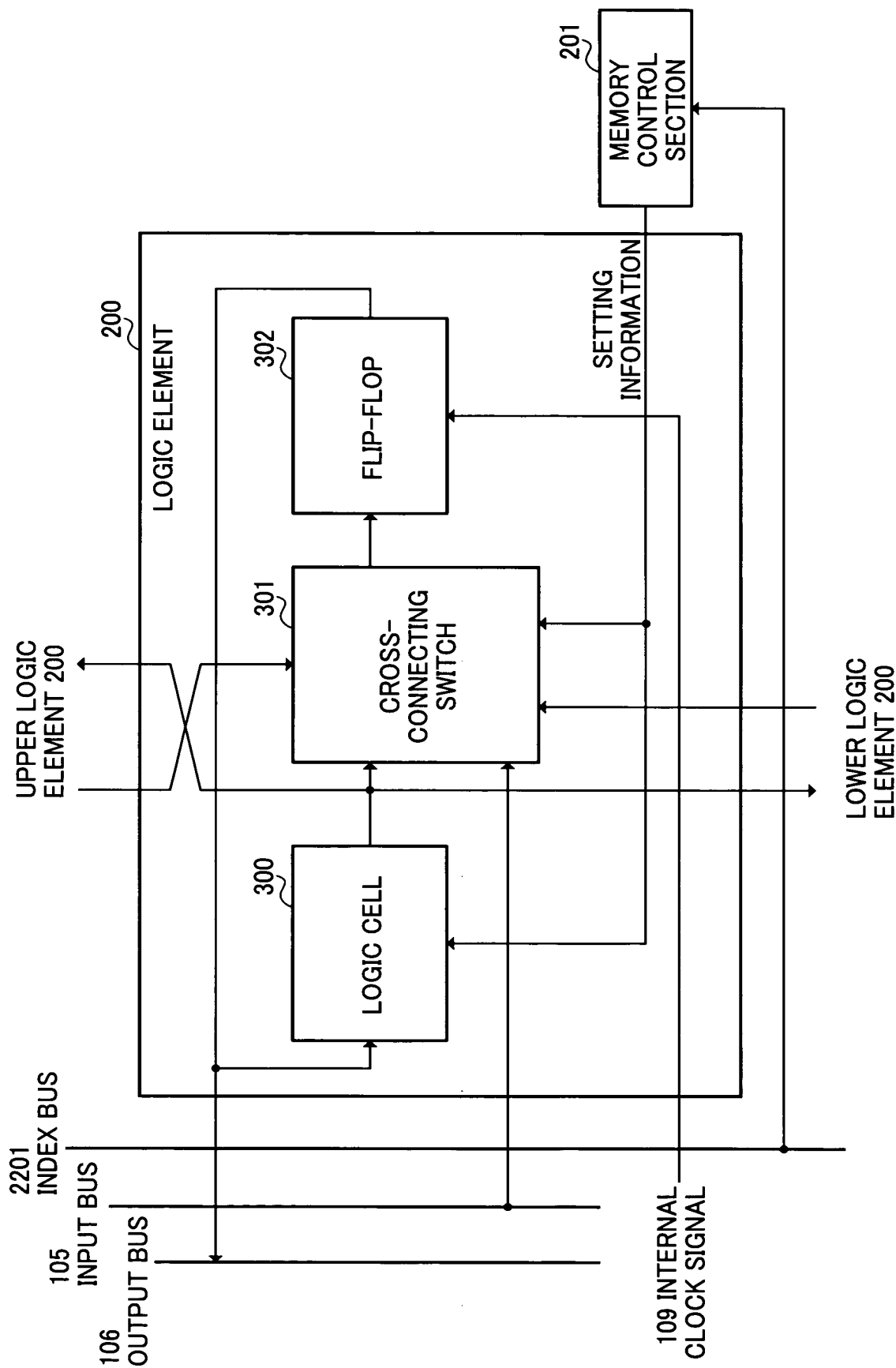


FIG.24

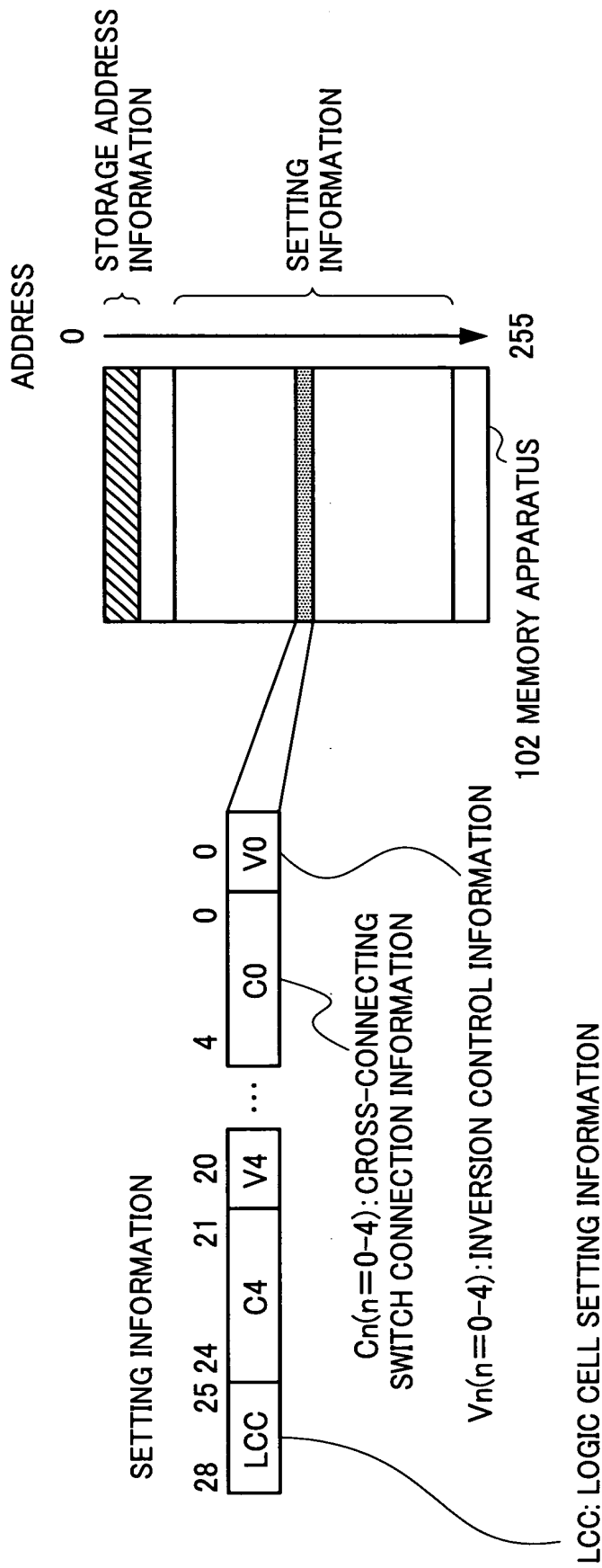


FIG.25